



Novel NanoScale MOSFET with TCAD: Use of TCAD tool for the fabrication of nanoscale bulk MOSFET and Its Performance Investigation

By Intekhab Amin, Shah Alam

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Scaling of MOSFET is being carried out through several decades and we moved from micron to nano scale region. Scaling improves performance but at the same time it has some adverse effect, because as MOSFET is scaled down the source and drain come so close to each other so that the gate is losing control over the channel is called short channel effect. Here work has been carried out by doing engineering fabrication technique to reduce its SCE under 40nm channel length of an engineered device and is compared the non-engineered device of having same technology to have better short channel immunity of an engineered device as compared to that of non-engineered device. Since scaling means scaling of its supply voltage also if not then electric field will become a severe factor creates impact ionization and hence its performance by creating electron-holes pair so supply voltage has to be scaled down to reduce peak electric field. Its high frequency small signal analysis is also carried out and compared.

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Editorial Review

About the Author

The author have done A.M.I.E in Electronics & Communication Engineering and was project associate at IIT Madras in the department of Electrical Engineering, he also did his M.Tech from Z.H.C.E.T AMU aligarh and presently (2012) doing his Ph.D from NITJ.

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